

DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: Semiconductor Transistor Devices and Methods for Forming Semiconductor Transistor Devices, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

POWER OF ATTORNEY:

As a named Inventor, I hereby appoint the following attorneys and agent to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Richard J. St. John, Reg. No. 19,363; David P. Roberts, Reg. No. 23,032; Randy A. Gregory, Reg. No. 30,386; Mark S. Matkin, Reg. No. 32,268; James L. Price, Reg.

1 No. 27,376; Deepak Malhotra, Reg. No. 33,560; Mark W. Hendricksen,
2 Reg. No. 32,356; David G. Latwesen, Reg. No. 38,533; George G.
3 Grigel, Reg. No. 31,166; Keith D. Grzelak, Reg. No. 37,144; John S.
4 Reid, Reg. No. 36,369; Lance R. Sadler, Reg. No. 38,605; W. Bryan
5 Farney, Reg. No. 32,651; Lia M. Pappas, Reg. No. 34,095; and Michael
6 L. Lynch, Reg. No. 30,871.

7 Send correspondence to: WELLS, ST. JOHN, ROBERTS,
8 GREGORY & MATKIN P.S., 601 W. First Avenue, Suite 1300, Spokane,
9 WA 99204-0317. Direct telephone calls to: David G. Latwesen,
10 (509) 624-4276.

11 I hereby declare that all statements made herein of my own
12 knowledge are true and that all statements made on information and
13 belief are believed to be true; and further that these statements were
14 made with the knowledge that willful false statements and the like so
15 made are punishable by fine or imprisonment, or both, under
16 Section 1001 of Title 18 of the United States Code and that such willful
17 false statement may jeopardize the validity of the application or any
18 patent issued therefrom.

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24

* * * * *

Full name of inventor: Aftab Ahmad

Inventor's Signature: _____

[Handwritten Signature]

Date: _____

6/26/96

Residence: Boise, Idaho

Citizenship: USA

Post Office Address: 3426 South Pimmit Place
Boise, ID 83706

* * * * *

Full name of inventor: David J. Keller

Inventor's Signature: _____

[Handwritten Signature]

Date: _____

6-26-96

Residence: Boise, Idaho

Citizenship: USA

Post Office Address: 2206 Roanoke
Boise, ID 83712

TELETYPE UNIT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ahmad, Aftab ; Keller, David J.

Application No.: 09/167,174 Group No.: 2811
Filed: October 6, 1998 Examiner: O. Nadav
For: Semiconductor Transistor Devices and Methods for Forming Semiconductor Transistor
Devices.

Assistant Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY (37 C.F.R. 1.34)

Please recognize as Associate Practitioner in this case:

Name of practitioner: James E. Lake
Address: W. 601 First Ave., Suite 1300
Spokane, WA 99201-3828

Reg. No. 44,854
Tel. No. (509) 624-4276

Reg. No. 38,533
Tel. No.: (509) 624-4276
Customer No.: 021567



SIGNATURE OF PRINCIPAL
PRACTITIONER OF RECORD
David G. Latwesen, Ph.D.
601 W. First Ave., Suite 1300
Spokane, WA 99201-3828

EL 844053675

09/167,174